

sdmay19-47: NSF Lab furnace control system

Week 4 Report

February 13th - February 22nd

Team MembersAdam Matthews — *Software/Hardware Engineer, Report Manager*Kevin Lang — *Electrical Engineer*Jeremy Hartl — *Hardware Engineer*Christopher Pohlen — *Software Engineer/Gitlab Moderator*Nick Brylski — *Systems Engineer***Advisor/Client**

Dr. Gary Tuttle

Summary of Progress this Report

The main progress we made in this report was the wiring of the schematic in in eagle PCB. This includes all surface mount ICs, passive components, and connectors. We have also met up and discussed the presentation, and plans for the API.

Pending Issues

The components on the PCB need placement and routing.

API and GUI need more fleshing out, with more requirements needing to be satisfied.

Individual Contributions

Team Member	Contribution	Weekly Hours	Total Hours
Adam Matthews	<ul style="list-style-type: none">Continuing to turn OTC API into module	4	13
Kevin Lang	<ul style="list-style-type: none">PCB schematic	3	12
Jeremy Hartl	<ul style="list-style-type: none">GUI development<ul style="list-style-type: none">Added furnace 2 and 3 tabs	3	9
Christopher Pohlen	<ul style="list-style-type: none">GUI development	3	12
Nick Brylski	<ul style="list-style-type: none">PCB design	3	12

Gitlab Activity Summary

Nothing to report.

